



**Architectural  
Specifications**

## 8002MCB-PHNX



[04/12/01]

### Architectural Specification for the Intelix 8002MCB-PHNX Mic/Line Mixer

The microphone mixer shall provide eight microphone/line level inputs and two line level, balanced outputs. All input and output connectors shall be Phoenix-type connectors. Front panel switches shall distribute the source to the main or auxiliary bus circuits. Each input shall have an input gain trim adjustment control on the rear panel and the adjustment range shall be 40 dB. Each input shall have a rear panel mic/line selector switch. Each input shall have a front panel low cut switch. The low cut circuit shall have a -3dB down point at 150Hz and a 12dB/octave response characteristic. All eight inputs shall have phantom power capability. The rear panel shall contain a common on/off switch for the phantom power to all inputs. Each input's line input circuitry shall prevent phantom power on that input when in line level mode. The output circuitry shall be of the active balanced, cross-coupled design. The output shall be capable of at least +26dBV balanced output levels. A rear panel -50dBV microphone pad switch shall be provided for the main output. A front panel output level bargraph indicator shall be provided. The bargraph shall have selectable average or peak reading capability. Peak reading shall be according to DIN specification 45406. The microphone mixer shall contain an output limiter circuit. The limiter shall have a threshold adjustment range of -20dB to -40dB output levels and ratio adjustment from 0-100%. A front panel on/off switch for the limiter circuit shall be provided.